On page 33, line 2: In Table V, in the fourth line of the heading for the sixth column, delete "BPSK".

On page 34, line 28: After "spread-spectrum" change the "." to a space.

IN THE CLAIMS

Please cancel claim 1, and add new claims 2-4 as follows.

1. (canceled)

2. (new) An assembly of simultaneously transmitted electromagnetic signals, said signals being related to each other in said assembly so as to communicate stored information to a receiver, said signals being generated by modulating selected subsets of a set of shift-register-stored binary spreading-code sequences onto a sinusoidal carrier, at least one subset of said set of binary spreading-code sequences containing more than one of said binary spreading-code sequences, each subset of said set of binary spreading-code sequences embodying a corresponding portion of said information.

3. (new) An assembly of simultaneously transmitted electromagnetic signals, said signals being related to each other in said assembly so as to communicate stored information within a transmitting node to a receiving node of a communication network, said assembly of signals being produced by a process of:

a) assigning blocks of bits embodying said stored information to corresponding subsets of a set of shift-register-stored binary spreading-code sequences, at least one of said subsets of said set of binary spreading-

code sequences containing more than one of said binary spreading-code sequences; and

b) simultaneously transmitting selected subsets of said set of stored binary spreading-code sequences from said transmitting node to said receiving node.

4. (new) An assembly of electromagnetic signals, said signals being related to each other in said assembly so as to communicate stored information within a transmitting node to a receiving node of a communication network, said assembly of signals being produced by a process of:

a) generating a set of binary spreading-code sequences by combining contents stored within specified stages of a first binary shift register with contents stored within specified stages of a second binary shift register, said set of binary spreading-code sequences containing more than one binary spreading-code sequence;

b) assigning blocks of bits embodying said stored information to corresponding subsets of said set of binary spreading-code sequences, each of said subsets of said set of binary spreading-code sequences containing at least one of said binary spreading-code sequences; and

c) transmitting selected subsets of said set of binary spreading-code sequences from said transmitting node to said receiving node.

IN THE ABSTRACT

Please add the following abstract of the disclosure. As preferred by the USPTO, the abstract is being presented on a separate page.